

05-12-04

Dfw



PTO/SB/21 (08-03)

Approved for use through 08/30/2003. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

| | |
|------------------------|---------------|
| Application Number | 10/618,471 |
| Filing Date | July 11, 2003 |
| First Named Inventor | Luan C. Tran |
| Art Unit | 2812 |
| Examiner Name | Unknown |
| Attorney Docket Number | MI22-2331 |

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

| | | |
|--|--|---|
| <input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ | <input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please Identify below): Return Receipt Postcard, PTO-1449, Cited Reference |
|--|--|---|

Remarks

Customer No. 021567

No fee is required with this filing. However, should a fee be determined as owing, charge such fee to Deposit Account No. 23-0925.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

| | |
|-------------------------|---|
| Firm or Individual name | Satheesh K. Karra, Reg. No. 40,246 Wells St. John P.S. |
| Signature | <i>S. Satheesh</i> |
| Date | May 10, 2004 |

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

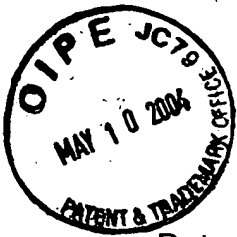
| | |
|-----------------------|---|
| Typed or printed name | Mailed via Express Mail EV 372452808 US |
|-----------------------|---|

| | | | |
|-----------|--|------|--|
| Signature | | Date | |
|-----------|--|------|--|

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

EV 372452808



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/618,471
Filing Date July 11, 2003
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Unknown
Attorney Docket No. MI22-2331
Title: Semiconductor Processing Methods of Forming Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Pursuant to 1276 OG 55, August 5, 2003, no copies of cited U.S. patents or U.S. patent application publications are included, as the date of filing of this patent application occurs after June 30, 2003. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

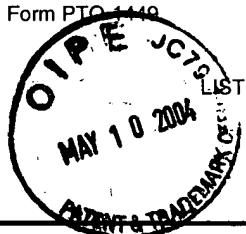
Dated: _____

May 10, 2004

By: _____

K. Satheesh K.

Satheesh K. Karra
Reg. No. 40,246

| | | | | | | | |
|--|--|--|--|-------------------------------|--|--------------------------|--|
| Form PTO 1449  | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | ATTY. DOCKET NO. MI22-2331 | | SERIAL NO. 10/618,471 | |
| LIST OF ART CITED BY APPLICANT (Use several sheets if necessary) | | | | APPLICANT: Luan C. Tran | | | |
| FILING DATE July 11, 2003 | | | | GROUP 2812 | | | |

| U.S. PATENT DOCUMENTS | | | | | | | |
|-----------------------|----|-----------------|---------|---------------|-------|----------|----------------------------|
| *Examiner's Initials | | Document Number | Date | Name | Class | Subclass | Filing Date If Appropriate |
| | AA | 6,054,730 | 04/2000 | Noguchi | 257 | 306 | |
| | AB | 6,466,489 | 10/2002 | leong et al. | 365 | 189.09 | |
| | AC | 6,579,751 | 06/2003 | Tran | 438 | 194 | |
| | AD | 6,200,863 | 03/2001 | Xiang et al. | 438 | 286 | |
| | AE | 6,238,967 | 05/2001 | Shiho et al. | 438 | 244 | |
| | AF | 6,620,679 | 09/2003 | Tzeng et al. | 438 | 250 | |
| | AG | 6,008,080 | 12/1999 | Chuang et al. | 438 | 200 | |
| | AH | 5,834,851 | 11/1998 | Ikeda et al. | 257 | 903 | |
| | AI | 5,371,026 | 12/1994 | Hayden et al. | 437 | 41 | |
| | AJ | 2003/0205745 | 11/2003 | Nam | 257 | 296 | |

| FOREIGN PATENT DOCUMENTS | | | | | | | | |
|--------------------------|----|-----------------|------|---------|-------|----------|-------------|----|
| | | Document Number | Date | Country | Class | Subclass | Translation | |
| | | | | | | | Yes | No |
| | AJ | | | | | | | |
| | AK | | | | | | | |
| | AL | | | | | | | |

| OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) | | | |
|---|----|-----------------|--|
| | AM | | S. J. Ahn et al., "Novel DRAM Cell Transistor with Asymmetric Source and Drain Junction Profiles Improving |
| | | | Data Retention Characteristics" IEEE, 2002, pp. 176-177. |
| | AN | | |
| | | | |
| | AO | | |
| | | | |
| EXAMINER | | DATE CONSIDERED | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.